



原文链接: <https://blog.csdn.net/yangtuoll12233/article/details/136455151>

[illegible][illegible][illegible]

The schematic diagram shows a PIC18F46K20 microcontroller (U4) connected to various components. The microcontroller's pins are connected as follows: VDD to 3.3V, VSS to GND, OSC1 to C3, OSC2 to C4, CLKOUT to C5, SCL to D6, SDA to BAT1, and SPI pins (CS, MOSI, MISO) to TP1, TP2, and TP3. The microcontroller is also connected to a MicroSD card (U5) via its I2C and SPI interfaces. The MicroSD card is connected to the microcontroller's I2C and SPI interfaces. The circuit is powered by a 3.3V supply.

The schematic diagram illustrates the test circuit for the PCF8563T. It features a 12V power supply connected to the circuit through a 100nF decoupling capacitor and a 1N5819WS diode. The PCF8563T is connected to a MicroSD card (U4) via its I2C pins: OSC1, OSC0, CLKOUT, SCL, and SDA. The card's pins are labeled: CMD (3), CSD (4), DAT0 (5), DAT1 (6), GND (7), DAT2 (8), DAT3 (9), and GND (10). The card is also connected to a 3.3V supply and a 10kΩ pull-up resistor (R19). The test points TP1, TP2, and TP3 are indicated.

[illegible][illegible][illegible]

The schematic diagram illustrates the test setup for the DCFS8563T/5,518. It shows a power supply connected to a 3.3V pin (R19, 10KΩ) and a TEST pin (TP3, 10KΩ). A CL10C200J88NNNC capacitor is connected to the TEST pin. A MicroSD card is connected to the 3.3V pin. The ground (GND) is connected to the TEST pin.

Figure 1: Schematic diagram of the test circuit. The circuit includes a component labeled CL10C200JB8NNNC, a TEST point, two 10KΩ (1002) ±1% resistors, and a 3.3V supply. The component is connected to GND. One resistor connects the TEST point to the 3.3V supply, and the other connects the 3.3V supply to the component.

1	2	3
---	---	---



Schematic1			创建日期	2025-02-07
			更新日期	2025-02-28
Board1			图页	P1
	STC32G核心板			
	版本	尺寸	页 1 共 1	
	V1.0	A4	嘉立创EDA	
5			6	