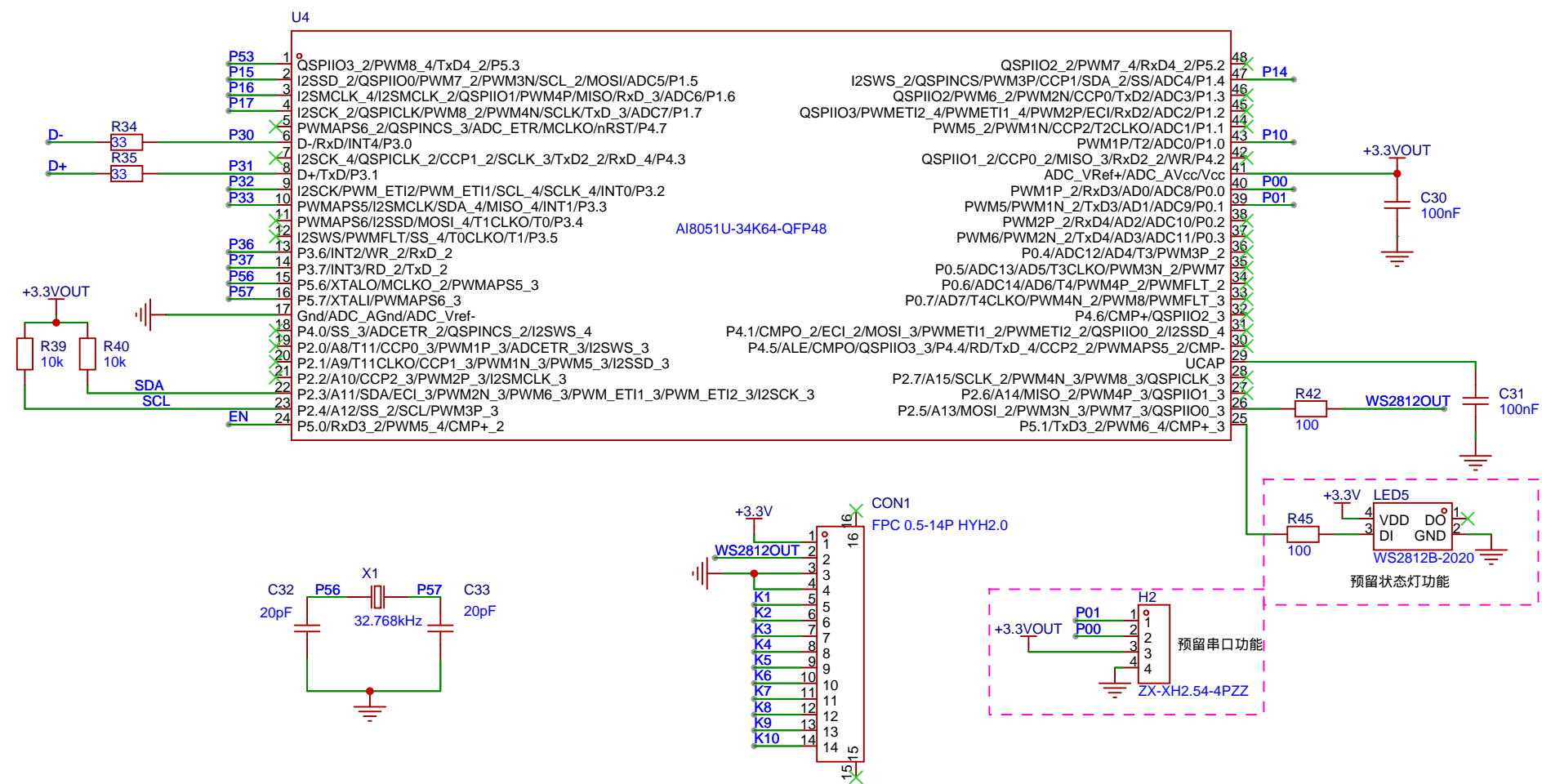
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The schematic shows the power input from SW11 connected to a fuse F2 (J-KMSMD500L-12V). The main power rail (+5V) includes a decoupling capacitor C45 (100µF) and a Schottky diode D8 (SS54). A secondary +5V output is derived from a USB port via J5B2, with decoupling capacitors R55 (5.1k) and R57 (1.1k). This +5VOUT rail passes through a resistor R56 (1k) and a LED before entering the VIN pin of the AMS1117-3.3 voltage regulator (U6). The regulator's GND pin is connected to the common ground. Its VOUT (TAB) pin provides the +3.3V output, which is filtered by capacitors C47 (10µF) and C48 (100nF) and protected by a Schottky diode D7 (SMBJ3.3CA).

原理图	桌面级数字原声声卡+STC主控蓝牙BT401		更新日期	2025-04-12	
围页	P1		创建日期	2025-03-11	
绘制	桌面级数字原声声卡+STC主控蓝牙				
审阅					
	版本	尺寸	页 1 共 1		
嘉立创EDA		V1.0	A4	嘉立创EDA	