STC大学计划实验箱 9.6 原理图

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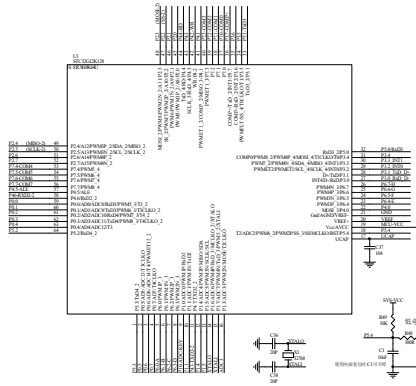
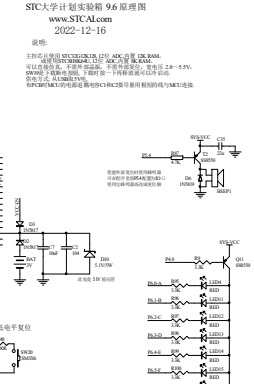
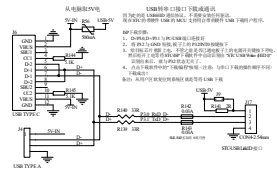
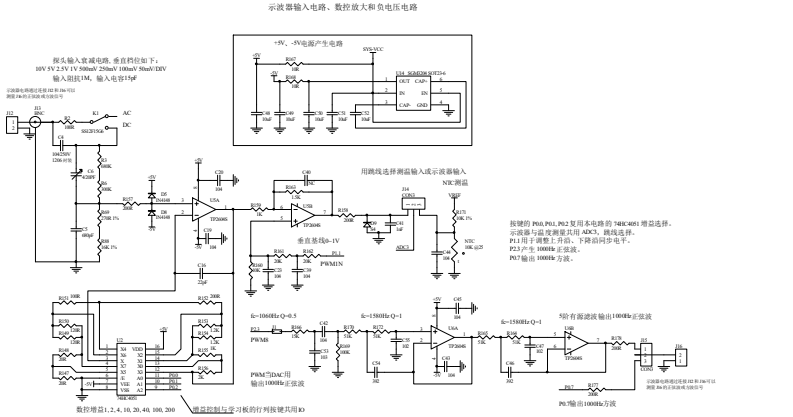
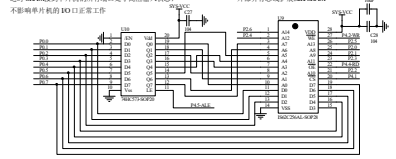
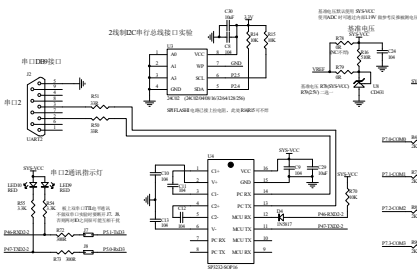
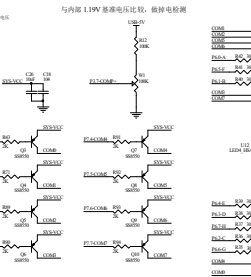
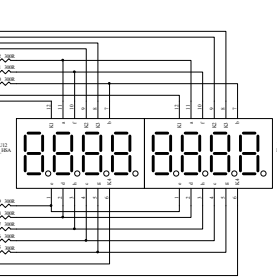
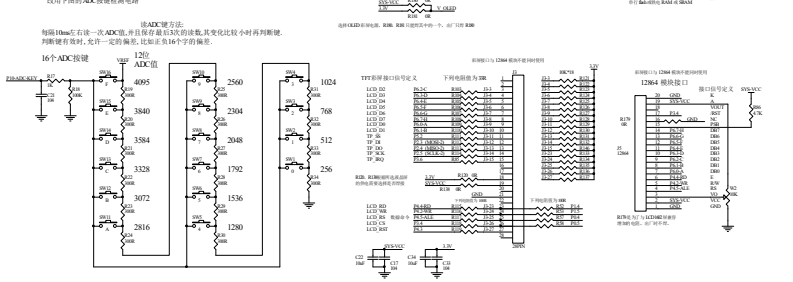
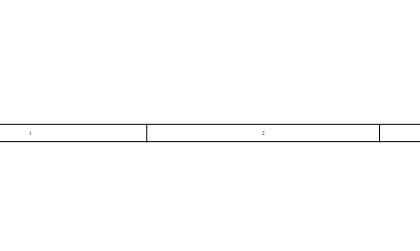
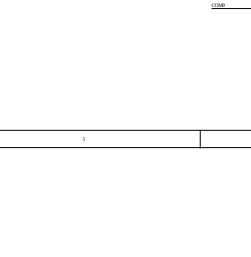
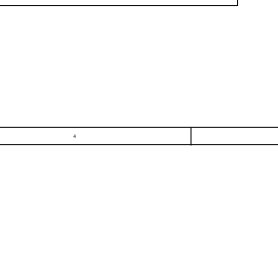
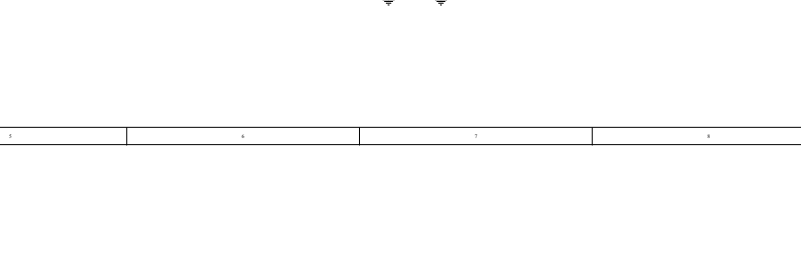
说明:

主控芯片使用 STC32G12K128, 12位 ADC, 内置 128K RAM.

或使用STC8H8K64U, 12位 ADC, 内置 64K RAM.

可以直接供电, 不使用 锂电池, 也使用 双节, 锂电压 2.0~5.5V.

SW1为 3 档拨动开关档位, 下拉时 按一下外部复位可以开启自动

供电方式 从USB取5V电.

ISP(CH9718A3)芯片通过 集电极NPN三极管 和 单片机的RX/TX 电平相反的电路与MCU3连接.